

FIG. 1A

VI ARCHITECTURAL MODEL

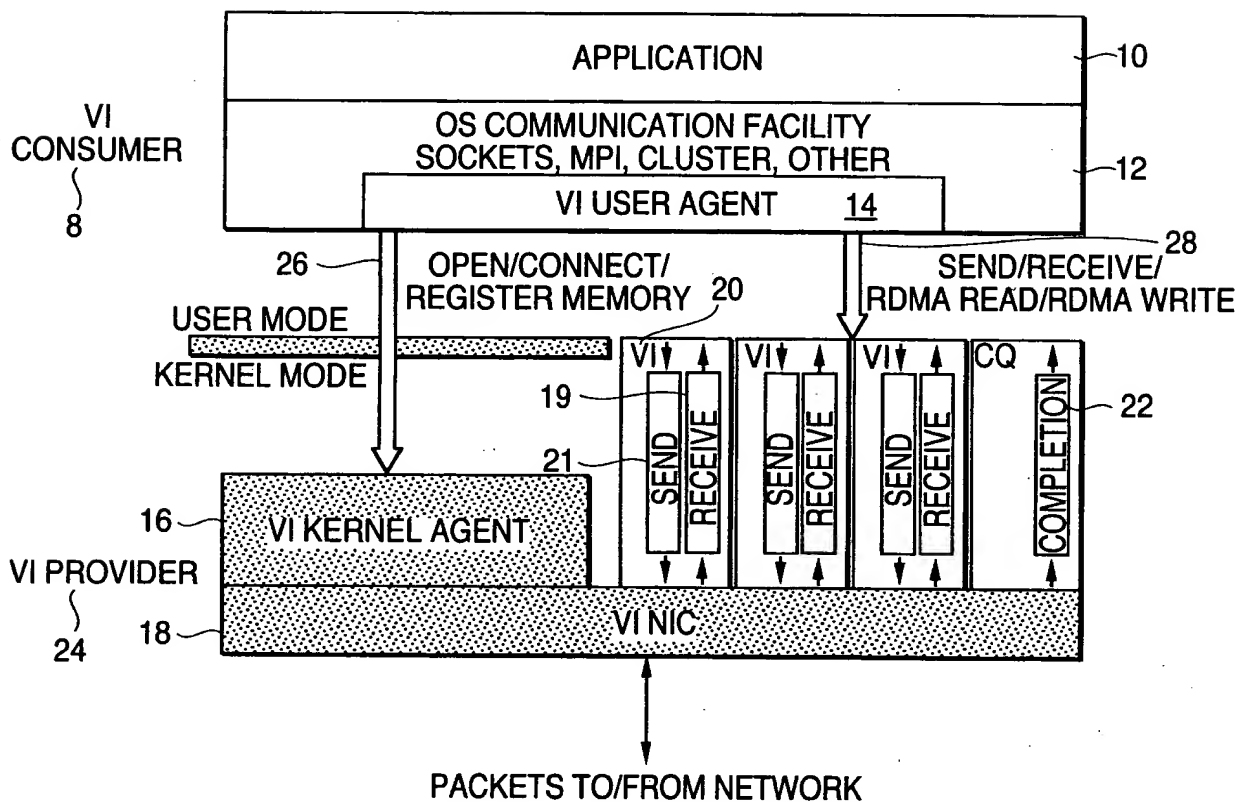


FIG. 1B

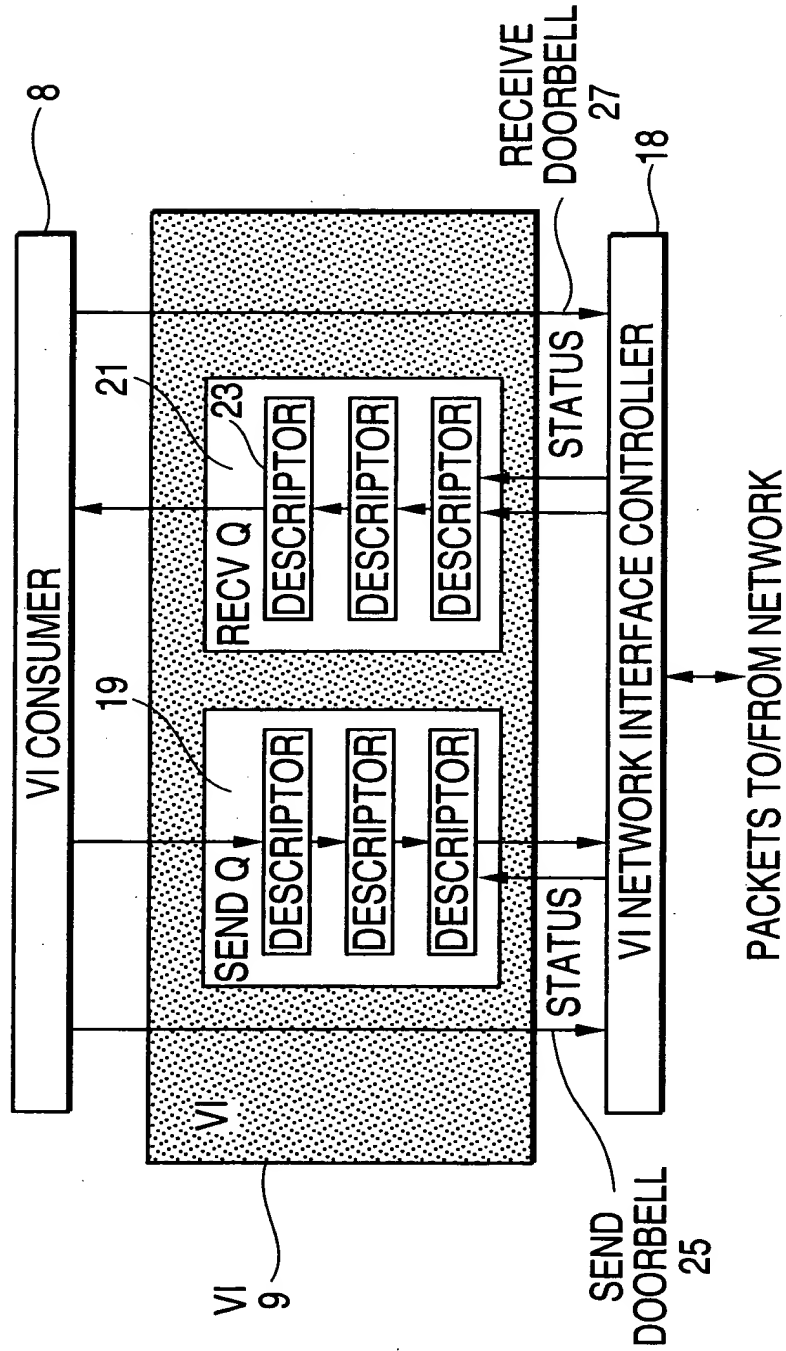


FIG. 2

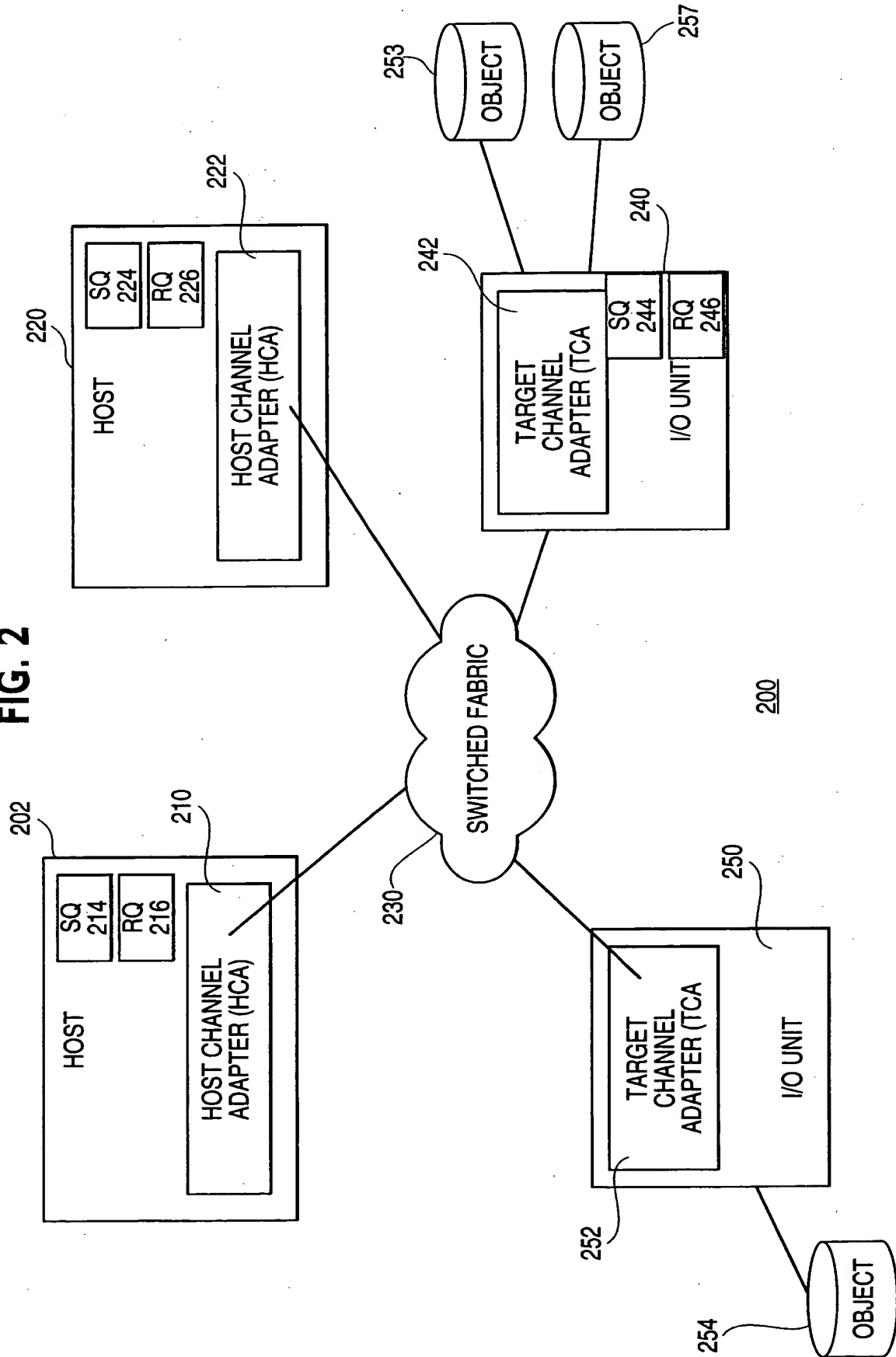
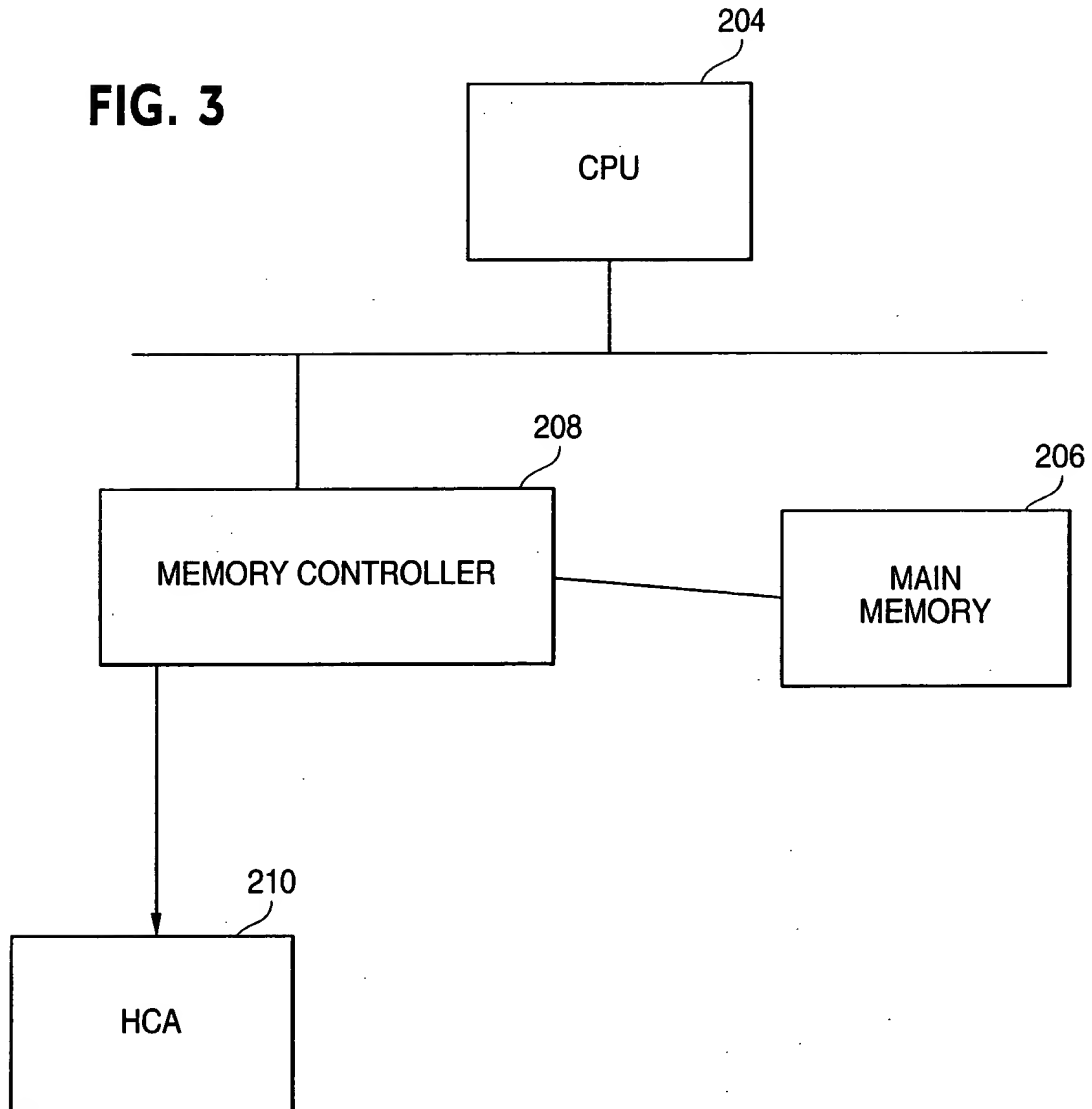
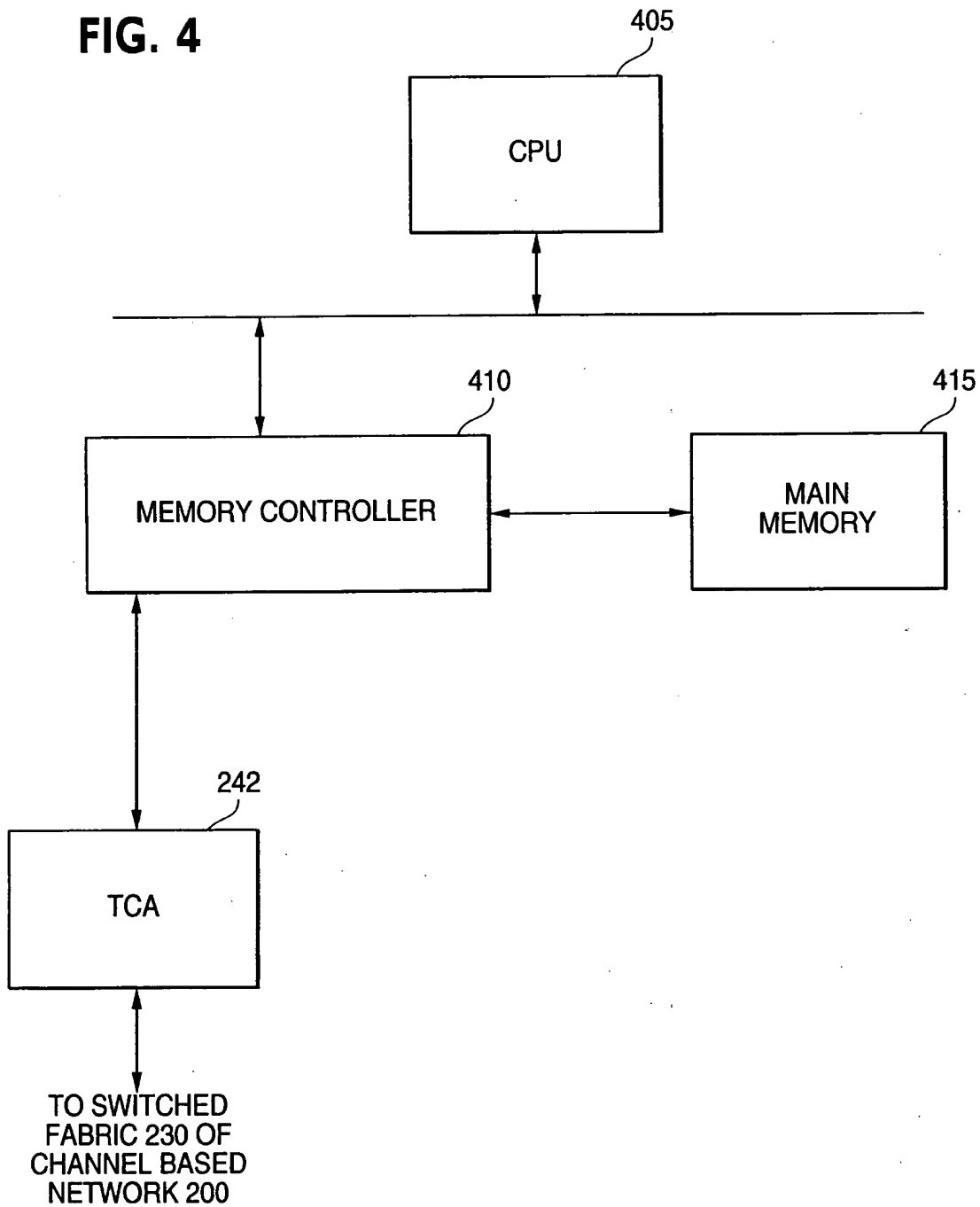


FIG. 3



HOST 202

FIG. 4



I/O UNIT 240

FIG. 5

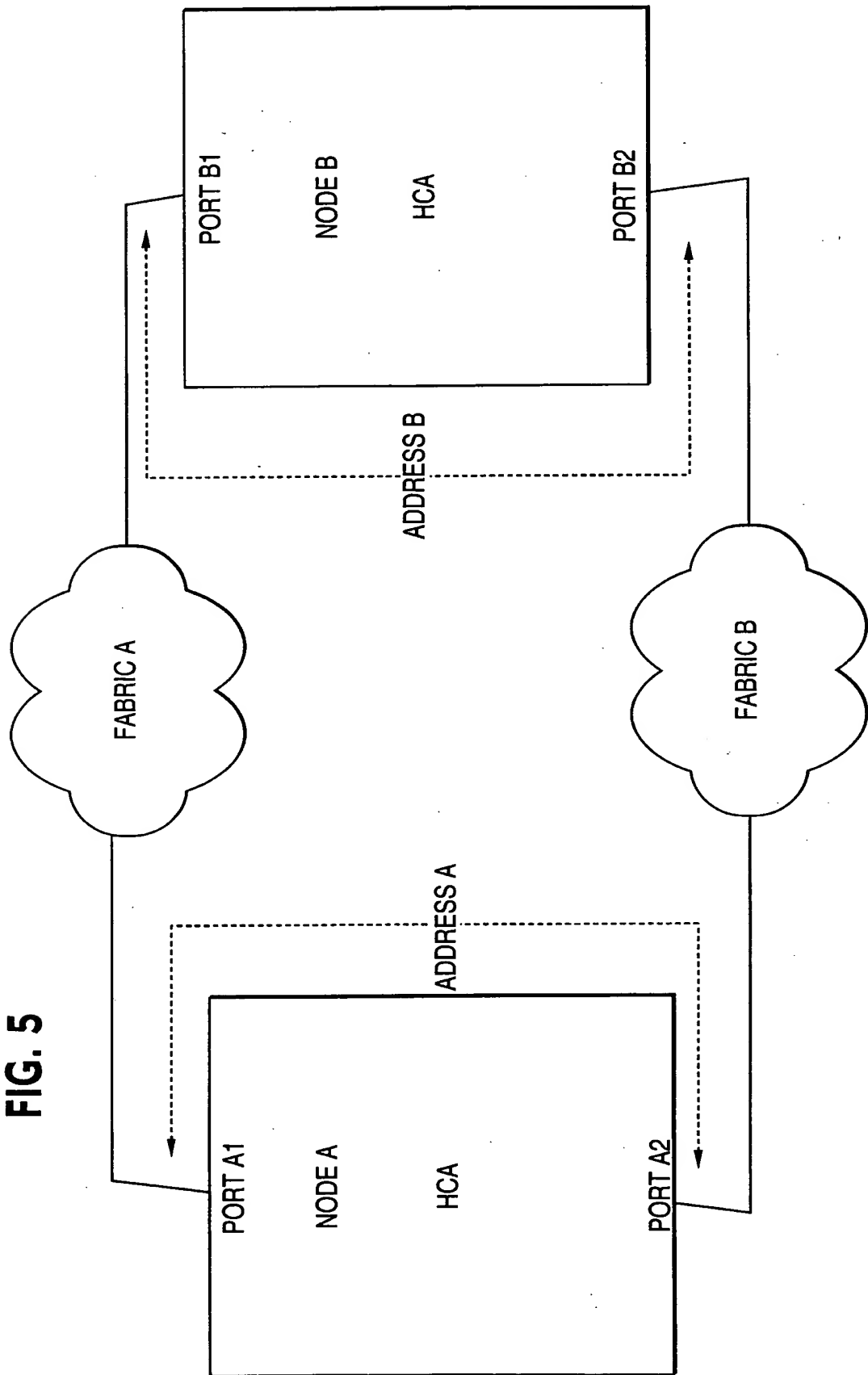


FIG. 6

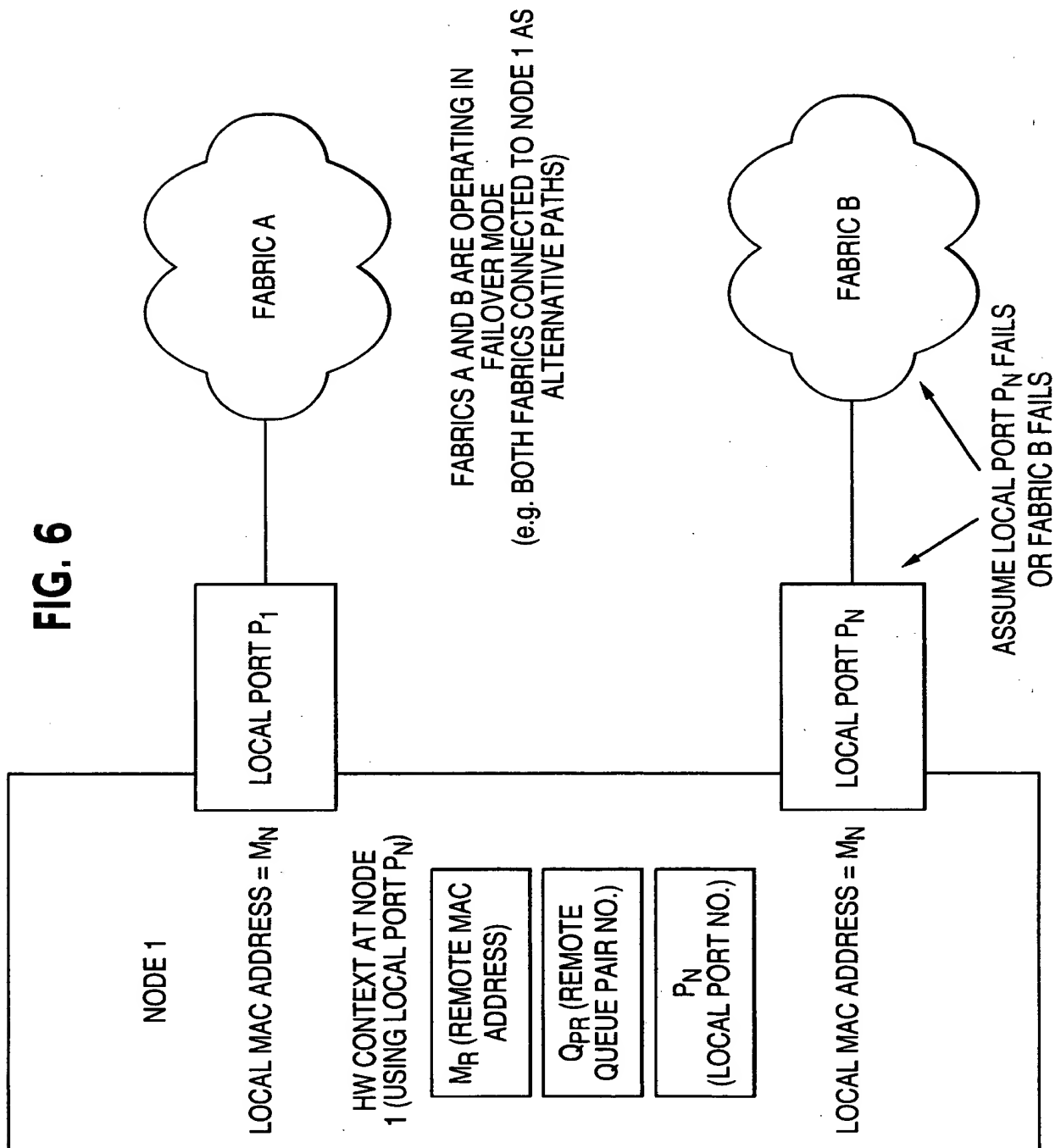


FIG. 7

VIRTUAL-TO-PHYSICAL PORT MAP	
VIRTUAL PORT	PHYSICAL PORT
VP1	PP6
VP2	PP1
VP3	PP7
VP4	PP4
VP5	PP2
VP6	PP3
VPN	PPN

FIG. 8

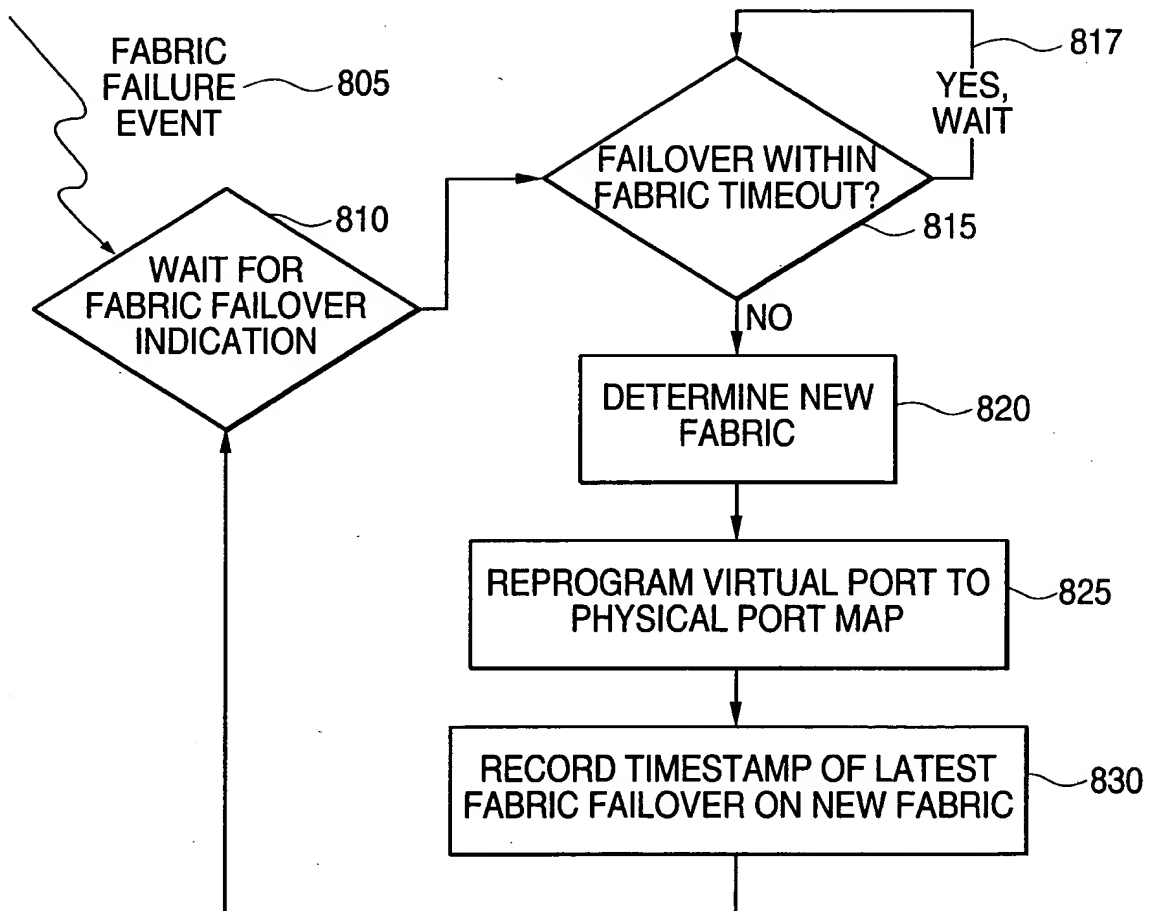


FIG. 9

